Amendments to the Claims

This listing of claims will replace all prior versions, and listings of claims in the application.

1.   (original) A memory access system comprising:

a register file having a plurality of registers each having the same predetermined bit capacity and capable of holding one or more objects depending on the bit length of the object, said registers including at least a first address register holding at least two packed objects for use in identifying respective memory addresses;

a register accessing circuit operative responsive to a memory access instruction to access said first address register in said register file;

address generating circuitry for generating at least first and second memory addresses from said at least two packed objects; and

memory access circuitry for accessing a memory using said first and second addresses.
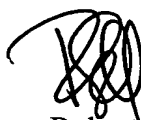
Please cancel claims 2-19

Remarks

Upon entry of the foregoing amendment, claim 1 is pending in the application. Claims 2-19 are sought to be cancelled without prejudice to or disclaimer of the subject matter therein.

Conclusion

Prompt and favorable consideration of this Preliminary Amendment is respectfully requested. Applicant(s) believe the present application is in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Robert Sokohl
Attorney for Applicant
Registration No. 36,013

Date: 10/29/03

1100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

188739-1